

U.S. Patent Application Serial No. 08/864,423 is a continuation-in-part of U.S. States Patent Application Serial No. 08/800,053 filed February 14, 1997 entitled "Method and Apparatus for Document Identification and Authentication," now issued as U.S. Pat. No. 5,992,601.--

IN THE CLAIMS:

Please cancel claims 1-156 and 159-163 and amend claims 157 and 158 as follows.

157. (Amended) A currency evaluation device comprising:

a discriminating unit [for evaluating] adapted to evaluate currency bills, [said] the discriminating unit including a detector, [said] the discriminating unit [determining] adapted to determine the denomination of said bills and [detecting] and to detect the occurrence of a plurality of error conditions;

an interface adapted to permit [permitting] a user of [said] the evaluation device to specify how [said] the plurality of error conditions are to be handled; and

a memory adapted to store [storing] user information specifying how [said] the plurality of error conditions are to be handled, the information being capable of subsequent recall and selection by a user of the evaluation device.

158. (Amended) A currency evaluation device for receiving a stack of currency bills and rapidly evaluating all the bills in the stack, [said] the device comprising:

an input receptacle [for receiving] adapted to receive a stack of bills to be evaluated;

an output receptacle [for receiving said] adapted to receive bills after [said bills have been evaluated] evaluation;

a transport mechanism [for transporting said] adapted to transport bills, one at a time, from [said] the input receptacle to [said] the output receptacle along a transport path;

a discriminating unit [for evaluating said] adapted to evaluate the bills, [said] the discriminating unit including a detector positioned along [said] the transport path between [said] the input receptacle and [said] the output receptacle, [said] the discriminating unit [determining] adapted to determine the denomination of [said] the bills and [detecting] to detect the occurrence of a plurality of error conditions;

a memory [storing] adapted to store information associated with a plurality of modes of operation of the device, [said] the memory being designed to store at least one user-defined